

These BCD-to-decimal decoders/drivers consist of eight inverters and ten, four-input NAND gates. The inverters are

connected in pairs to make BCD input data available for

decoding by the NAND gates. Full decoding of BCD input logic ensures that all outputs remain off for all invalid (10-15) binary input conditions. These decoders feature high-performance, NPN output transistors designed for use as indicator/relay drivers, or as open-collector logic-circuit drivers. The high-breakdown output transistors are compatible for interfacing with most MOS integrated circuits.

DM5445/DM7445 BCD to Decimal Decoders/Drivers

General Description

Features

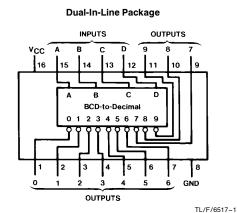
- Full decoding of input logic
- 80 mA sink-current capability

Function Table

Inputs

All outputs are off for invalid BCD input conditions

Connection Diagram



Order Number DM5445J, DM5445W or DM7445N See NS Package Number J16A, N16E or W16A

No.	mputo			Outputs										
	D	С	в	Α	0	1	2	3	4	5	6	7	8	9
0	L	L	L	L	L	н	н	н	Н	н	н	Н	Н	Н
1	L	L	L	Н	н	L	Н	Н	Н	Н	Н	Н	Н	Н
2	L	L	Н	L	н	Н	L	Н	Н	Н	Н	Н	Н	Н
3	L	L	Н	Н	н	Н	Н	L	Н	Н	Н	Н	Н	Н
4	L	Н	L	L	н	Н	Н	Н	L	Н	Н	Н	Н	Н
5	L	н	L	н	н	н	н	н	н	L	н	н	Н	н
6	L	Н	Н	L	н	Н	Н	Н	Н	Н	L	Н	Н	Н
7	L	Н	Н	Н	н	Н	Н	Н	Н	Н	Н	L	Н	Н
8	н	L	L	L	н	Н	Н	Н	Н	Н	Н	Н	L	Н
9	н	L	L	Н	н	Н	Н	Н	Н	Н	Н	Н	Н	L
I	н	L	н	L	н	н	н	н	н	н	н	н	н	н
Ν	н	L	Н	Н	н	Н	Н	Н	Н	Н	Н	Н	Н	Н
V	н	Н	L	L	н	Н	Н	Н	Н	Н	Н	Н	Н	Н
А	н	Н	L	Н	н	Н	Н	Н	Н	Н	Н	Н	Н	Н
L	н	Н	Н	L	н	Н	Н	Н	Н	Н	Н	Н	Н	Н
I	н	Н	Н	Н	н	Н	Н	Н	Н	Н	Н	Н	Н	Н
D														

Outputs

H = High Level (Off), L = Low Level (On)

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Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	5.5V
Output Voltage	30V
Operating Free Air Temperature Range	
DM54	-55°C to +125°C
DM74	0°C to +70°C
Storage Temperature Range	$-65^{\circ}C$ to $+150^{\circ}C$

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM5445			DM7445			Units
oynibol	i arameter	Min	Nom	Max	Min	Nom	Max	011110
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
VIL	Low Level Input Voltage			0.8			0.8	V
V _{OH}	High Level Output Voltage			30			30	V
IOL	Low Level Output Current			20			20	mA
T _A	Free Air Operating Temperature	-55		125	0		70	°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

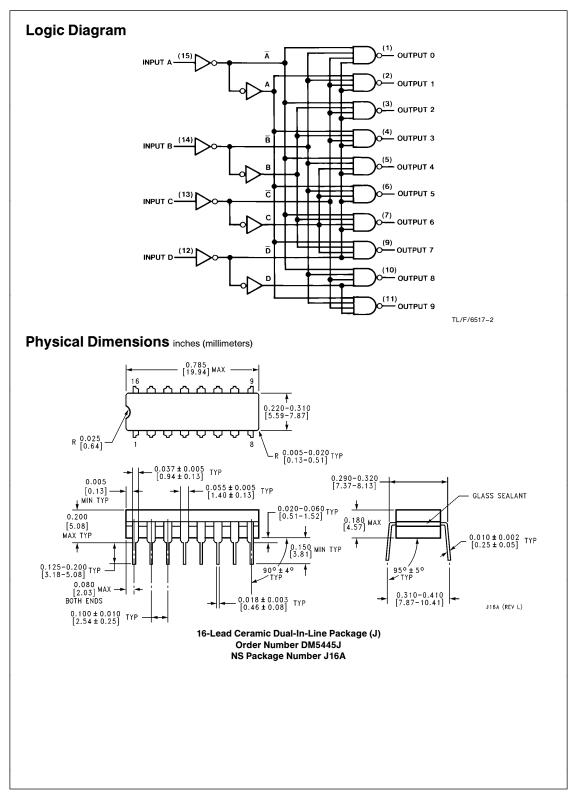
Symbol	Parameter	Conditio	ns	Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I =$	-12 mA			-1.5	V
ICEX	High Level Output Current	$\label{eq:V_CC} \begin{split} V_{CC} &= \text{Min}, \text{V}_{O} = 30\text{V} \\ V_{IL} &= \text{Max}, \text{V}_{IH} = \text{Min} \end{split}$				250	μA
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{OL}$ $V_{IH} = Min, V_{IL} =$			0.2	0.4	v
		$I_{OL} = 80 \text{ mA}$ $V_{CC} = Min$			0.5	0.9	v
l _l	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$				1	mA
I _{IH}	High Level Input Current	$V_{CC} = Max, V_I = 2.4V$				40	μΑ
IIL	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$				-1.6	mA
Icc	Supply Current	V _{CC} = Max	DM54		43	62	mA
		(Note 2)	DM74		43	70	

Switching Characteristics at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$ (See Section 1 for Test Waveforms and Output Load)

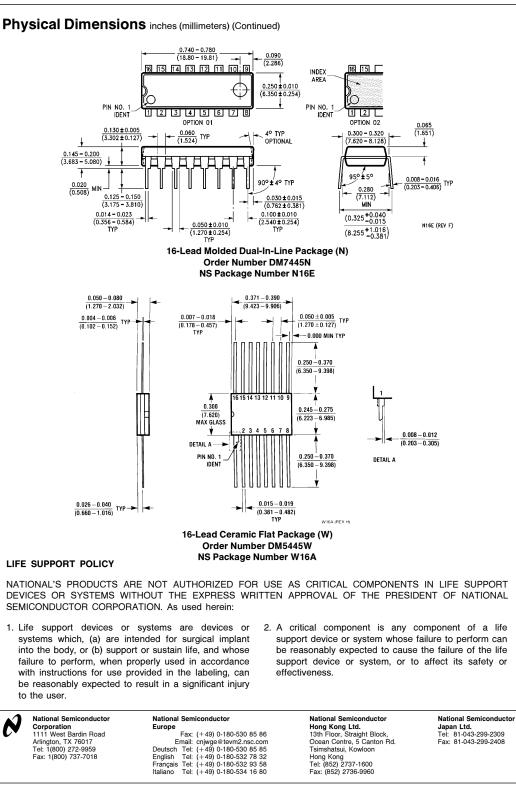
t_{PLH} Propagation Delay Time Low to High Level Output $C_L = 15 \text{ pF}$ $R_L = 100\Omega$ 49.5 ns t_{PHI} Propagation Delay Time $C_L = 15 \text{ pF}$ $A_{PL} = 100\Omega$ $A_{PL} = 100\Omega$						
Low to High Level Output $R_L = 100\Omega$ 49.5 ns tpHi Propagation Delay Time $R_L = 100\Omega$ $R_L = 100\Omega$ $R_L = 100\Omega$	Symbol	Parameter	Conditions	Min	Max	Units
t _{PHL} Propagation Delay Time 49.5 ps	t _{PLH}				49.5	ns
High to Love Level Output	t _{PHL}				49.5	ns

Note 1: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Note 2: $I_{\mbox{CC}}$ is measured with all inputs grounded and all outputs open.







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