National Semiconductor

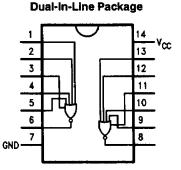
5425/DM7425 Dual 4-Input NOR Gate (with Strobe)

General Description

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This device contains two, 4-input gates that perform the logic NOR function. The output of each NOR gate is gated (strobed) by pin 3 and pin 11 by positive true logic i.e., logic "1" equals output on.

Connection Diagram



TL/F/9775-1 Order Number 5425DMQB, 5425FMQB, DM7425J or DM7425N See NS Package Number J14A, N14A and W14B

Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	5.5V
Operating Free Air Temperature Range	
54	-55°C to +125°C
DM74	0°C to +70°C
Storage Temperature Range	-65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	5425			DM7425			Units
		Min	Nom	Max	Min	Nom	Max	UIILS
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
VIH	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.8			0.8	v
ЮН	High Level Output Current			-0.8			-0.4	mA
l _{OL}	Low Level Output Current			16			16	mA
TA	Free Air Operating Temperature	- 55		125	0		70	℃

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	V_{CC} = Min, I _I = -12 mA				- 1.5	V
V _{OH}	High Level Output Voltage	V_{CC} = Min, I_{OH} = Max V_{IL} = Max		2.4	3.4		v
V _{OL}	Low Level Output Voltage	V _{CC} = Min, I _{OL} = Max V _{IH} = Min			0.2	0.4	v
li	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$				1	mA
Ι _{ΙΗ}	High Level Input Current	V _{CC} = Max,	Strobe			160	μΑ
	V _I = 2.4	$V_{I} = 2.4V$	Inputs			40	μη
۱	Low Level Input Current	V _{CC} = Max,	Strobe			-6.4	mA
	V ₁ = 0.4	V _I = 0.4V	Inputs			-1.6	1
los Short Circuit Output Current	Short Circuit	V _{CC} = Max	54	-20		55	mA
	Output Current	Output Current (Note 2)	(Note 2)	DM74	- 18		-57
ССН	Supply Current with Outputs High	V _{CC} = Max				16	mA
ICCL	Supply Current with Outputs Low	V _{CC} = Max				19	mA

Switching Characteristics at $V_{CC} = 5V$, $T_A = 25^{\circ}C$ (See Section 1 for Test Waveforms and Output Load)

Symbol	Parameter	Conditions	Min	Max	Units			
tPLH	Propagation Delay Time Low to High Level Output	$C_L = 15 pF$ $R_L = 400 \Omega$		22	ns			
t _{PHL}	Propagation Delay Time High to Low Level Output			15	ns			

Note 1: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Note 2: Not more than one output should be shorted at a time.

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Datasheets for electronic components.

National Semiconductor was acquired by Texas Instruments.

http://www.ti.com/corp/docs/investor_relations/pr_09_23_2011_national_semiconductor.html

This file is the datasheet for the following electronic components:

5425DMQB - http://www.ti.com/product/5425dmqb?HQS=TI-null-null-dscatalog-df-pf-null-wwe 5425FMQB - http://www.ti.com/product/5425fmqb?HQS=TI-null-null-dscatalog-df-pf-null-wwe DM7425J - http://www.ti.com/product/dm7425j?HQS=TI-null-null-dscatalog-df-pf-null-wwe DM7425N - http://www.ti.com/product/dm7425n?HQS=TI-null-null-dscatalog-df-pf-null-wwe