AN5763

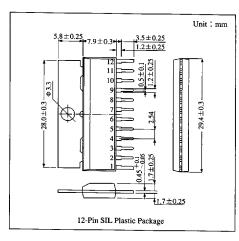
Vertical Deflection Signal Processing and Output IC for B/W TV

Overview

The AN5763 is one of the AN5700 series for 12V-operating Black/White TV. They are integrated circuits designed for B/W TV vertical deflection signal processing and output circuit.

Features

- Flyback pulse processing is highly efficient by pulse-up system.
- Level switch type oscillator circuit is incorporated, realizes economical circuitry with fewer external components.
- Vertical oscillator circuit featuring has highly stable operation against the change in temperature and supply voltage

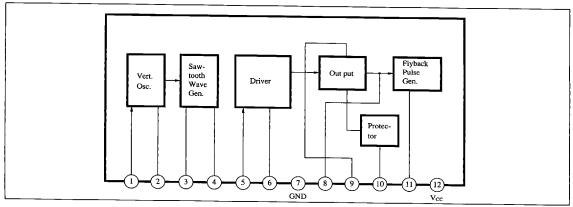


ICs for TV

Pin Descriptions

Till Descriptions				
Pin No.	Pin name			
1	Sync. input			
2	Saw-tooth wave generation			
3	Adj. for linearity			
4	Vert. Osc. output			
5	Input for vert. amp.			
6	Decoupling			
7	GND			
8	Vert. output			
9	Voltage source for vert. circuit			
10	Protector			
11	BLK pulse output			
12	Vcc			

Block Diagram



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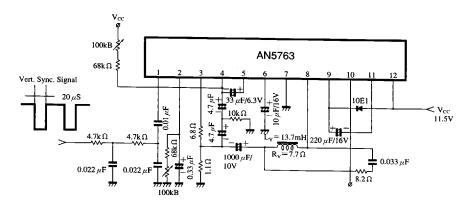
 \blacksquare Absolute Maximum Ratings (Ta=25%)

Parameter		Symbol	Rating	Unit	
Supply voltage		V_{cc}	15.6	V	
Power dissipation (Ta=70℃)		P _D	1330	mW	
Temperature	Operating ambient temperature	Topr	-20 to +70	r	
	Storage temperature	T _{stg}	-40 to +150	r	

\blacksquare Electrical Characteristics $(Ta=25^{\circ}C)$

Parameter	Symbol	Condition	min	typ	max	Unit
Circuit current(1)	I ₁₂	V _{CC} =11.5V, no-load	8.5	13.5	18.5	mA
Circuit current (2)	I ₁₂	$V_{CC} = 11.5V, R_L = 25 \Omega$	350	400	450	mA
Vertical oscillation-start voltage	V _{OSC-S(V)}	It shall be within f _{VO} =47 to 70Hz.	5.0			V
Vertical oscillation frequency	fvo	$V_{cc}=11.5V$	48	50	52	Hz
fvo supply voltage dependency	⊿f _{vo} /V _{cc}	f _{VO} 9.2V - f _{VO} 13.8V	_	0	1	Hz
fvo ambient temperature dependency	⊿f _{vo} /Ta	f _{vo} −20℃−f _{vo} 60℃		0	1	Hz
Vertical output pulse width	7 (VO)	V _{cc} =11.5V, Sync. state	250	330	400	μs
Vertical pull-in range	f _{VP}	V _{cc} =11.5V, Sync. state	18	20		Hz
Deflection current (Peak)	I _{y(P-P)}	$V_{\rm CC}=11.5$ V, Sync. state $R_{\rm H}=88$ k Ω	665	715	750	mA _{P-P}
Center voltage	V _{MID}	$V_{CC}=11.5V$, Sync. state $R_H=96.4k\Omega$	5.3	5.65	6.0	V
Flyback pulse amplitude	V _(FBP)	$V_{CC}=11.5V$, Sync. state $R_H=96.4k\Omega$	20	21.5		V
Blanking pulse width	T (BLP)	$V_{CC}=11.5V$, Sync. state $R_H=96.4k\Omega$	690	760	840	μs
Output Tr. saturation voltage	V ₁₂₋₈	$V_{CC} = 11.5V, R_L = 25 \Omega$		2.0	2.3	V

■ Application Circuit



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